Amir Mahdi Hosseini Monazzah

Room 332,
School of Computer Engineering,
Iran University of Science
and Technology (IUST),
Tehran, Iran. Zip Code: 16846-13114
☎ (+98 21) 7322-5347
☒ monazzah@iust.ac.ir
ṁ http://webpages.iust.ac.ir/monazzah
DOB: April 21, 1987



Curriculum Vitae

Education

- 2019-Present Iran University of Science and Technology, Tehran, Iran, Assistant Professor, Computer Engineering.
 - 2017–2019 **Institute for Research in Fundamental Sciences (IPM), Tehran, Iran**, *Post-doc, Computer Engineering*, Under Supervision of Prof. Hamid Sarbazi-Azad.
 - 2013–2017 **Sharif University of Technology, Tehran, Iran**, *Phd*, *Computer Engineering*, Under Supervision of Prof. Seyed Ghassem Miremadi. GPA: 19.38/20
 - 2016–2017 **University of California, Irvine, California, USA**, Sabbatical, Reliable Embedded Systems, Under Supervision of Prof. Nikil Dutt.
 - 2010–2012 **Sharif University of Technology, Tehran, Iran**, *M.Sc.*, *Computer Architecture*, Under Supervision of Prof. Seyed Ghassem Miremadi. GPA: 17.91/20
 - 2005–2009 **Islamic Azad University- South-Tehran Branch, Tehran, Iran**, *B.S.*, *Computer Hardware Engineering*, Under Supervision of Mr. Fatehi Khajeh.
 - 2001–2005 **Dr. Hesabi High School, Tehran, Iran**, *High School Diploma*, *Mathematics and Physics*. GPA: 18.8/20

Research Interests

- 1. On-chip memory design.
- 2. Fault-tolerant design.
- 3. System design for approximate computing applications.
- 4. System design for IoT applications.

Honors & Awards

- 1. Ranked 1^{st} during the PhD program on hardware computer engineering at Sharif University of Technology.
- 2. Ranked 3^{rd} among about 700 participants in PhD national entrance exam for graduate students in Computer Architecture.
- 3. Ranked 41^{st} among about 15,000 participants in M.Sc. national entrance exam for graduate students in Computer Architecture.
- 3. Ranked 2^{ed} among about 100 students in B.S..

Skills & Abilities

Programming Verilog HDL, VHDL, SystemC, C/C++

Languages

Tools GEM5, COOJA, NVSIM, CACTI, MODELSIM, HDL DESIGNER, DESIGN COMPILER, QUAR-

TUS, MATLAB (M-FILE PROGRAMMING, SIMULINK/STATEFLOW), CADENCE SOC EN-

COUNTER, HSPICE

Software LATEX, MS WORD, MS EXCEL

Teaching

2020-Fall Design of Fault-Tolerant Systems, Lecturer, Iran University of Science and Technology.

2020-Fall Microprocessor, Lecturer, Iran University of Science and Technology.

2020-Fall **Embedded and IoT Systems**, Lecturer, Sharif University of Technology.

2020-Spring **Digital Design**, Lecturer, Iran University of Science and Technology.

2020-Spring Advanced Design of Fault-Tolerant Systems, $\operatorname{Lecturer}$, Iran University of Science and

Technology.

2020-Spring **Embedded and IoT Systems**, Lecturer, Sharif University of Technology.

2019-Fall Design of Storage Systems, Lecturer, Iran University of Science and Technology.

2019-Fall Microprocessor, Lecturer, Iran University of Science and Technology.

2019-Spring Embedded and IoT Systems, Lecturer, Sharif University of Technology.

2018-Fall **Computer Architecture**, Lecturer, Sharif University of Technology.

2018-Fall Computer Architecture, Lecturer, Amirkabir University of Technology.

2018-Spring **Digital Design**, Lecturer, Iran University of Science and Technology.

Work Experience

Summer 2008 Internship, IRAN KHODRO INDUSTRIAL GROUP, IKCO, Tehran, Iran.

The Complex of Computer Systems Supports and Maintenance

Publications

Please google "Amir Mahdi Hosseini Monazzah" for an up to date list of my publications.

Languages

Farsi Native Speaker

English Fluent

Arabic Basic